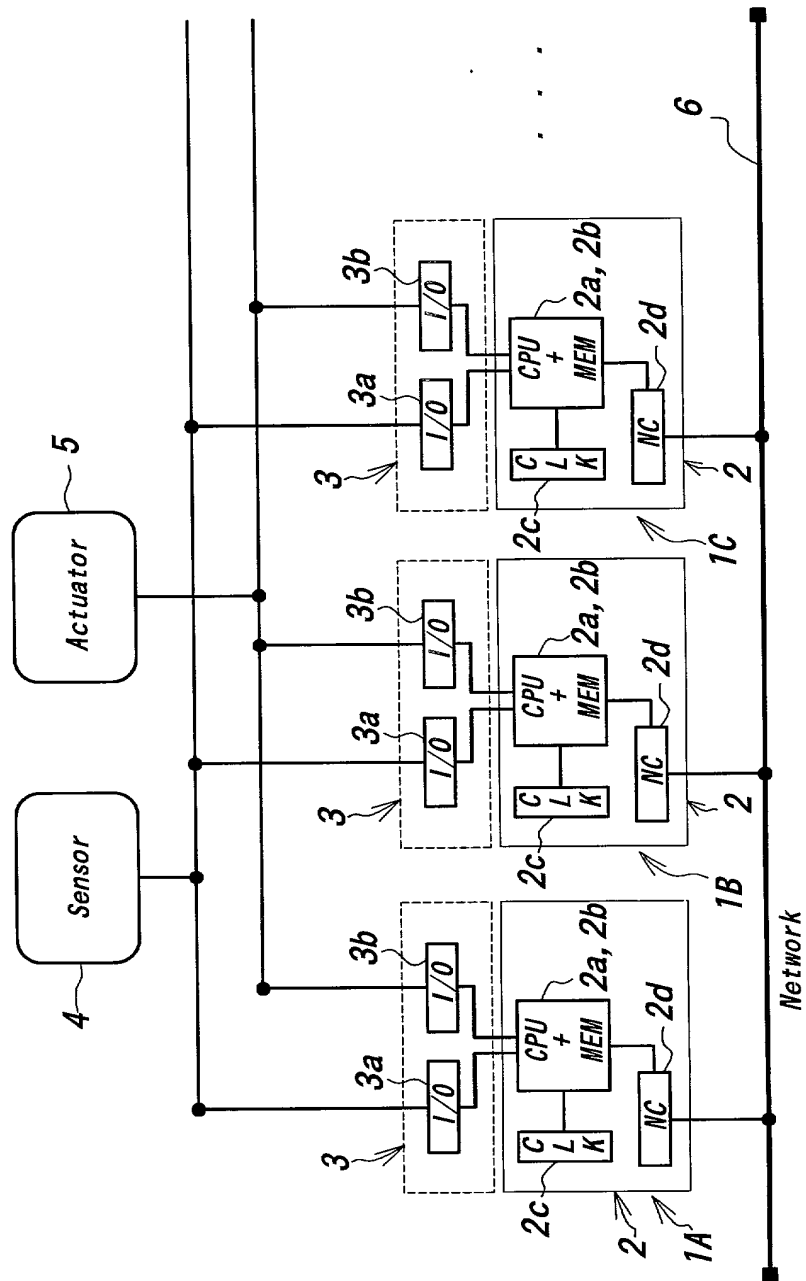


FIG. 1



# FIG. 2

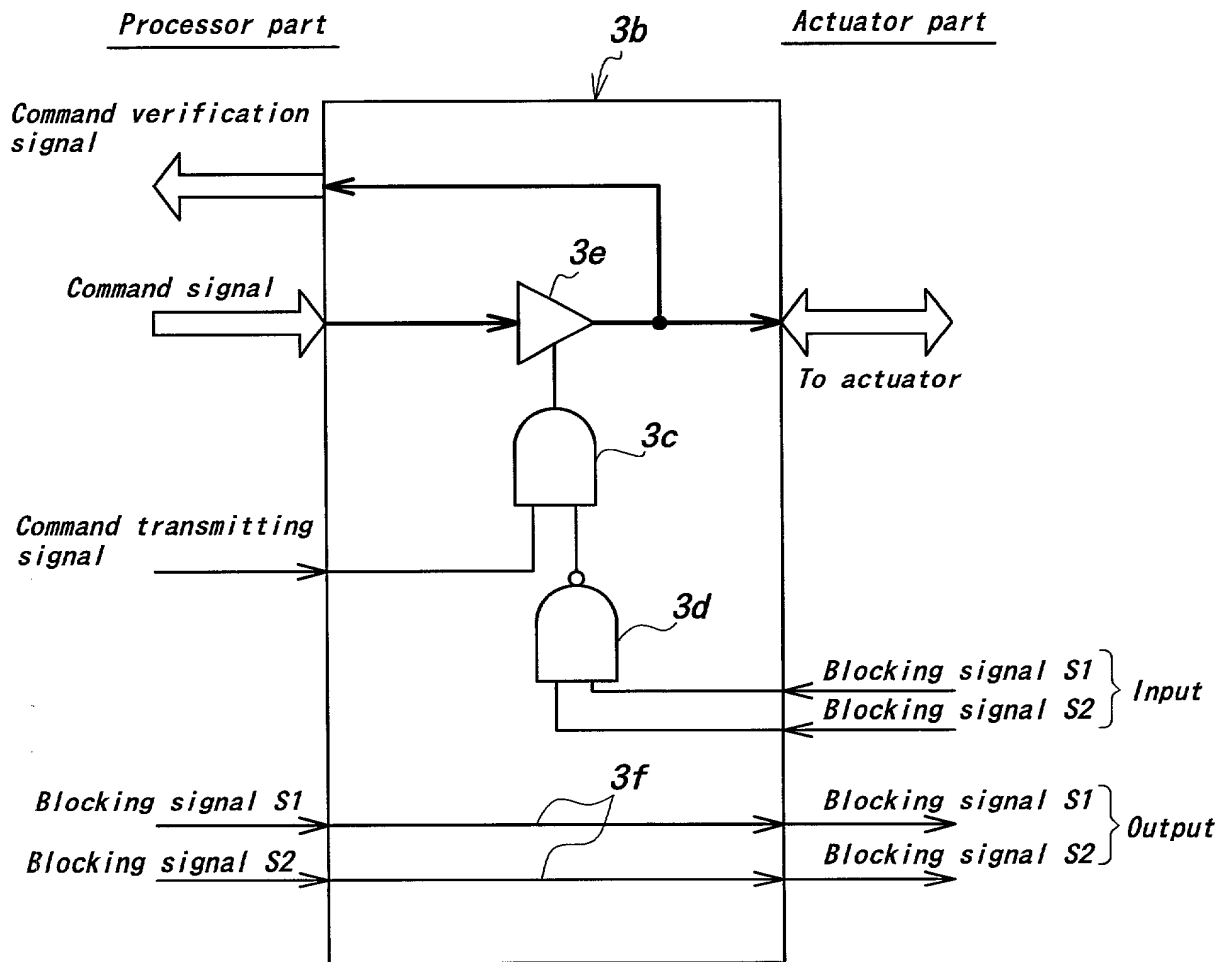
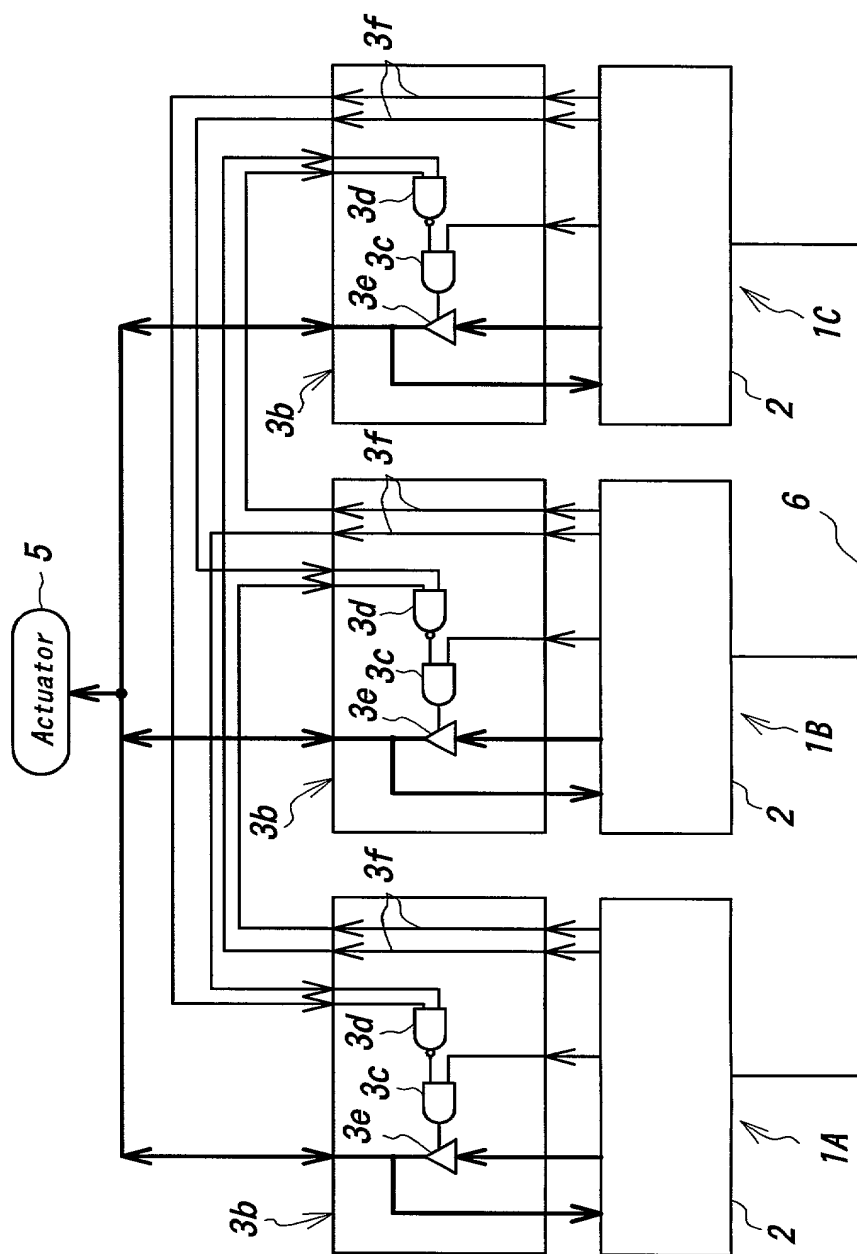
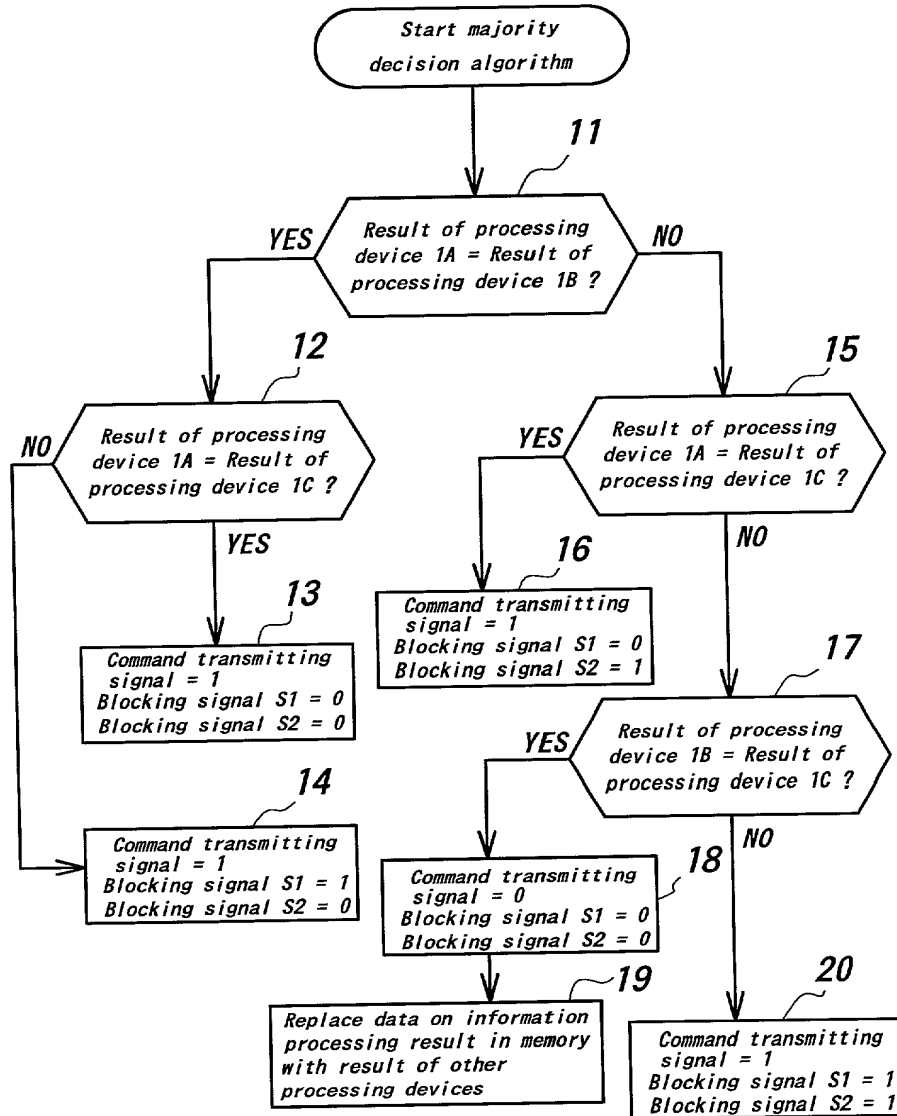


FIG. 3

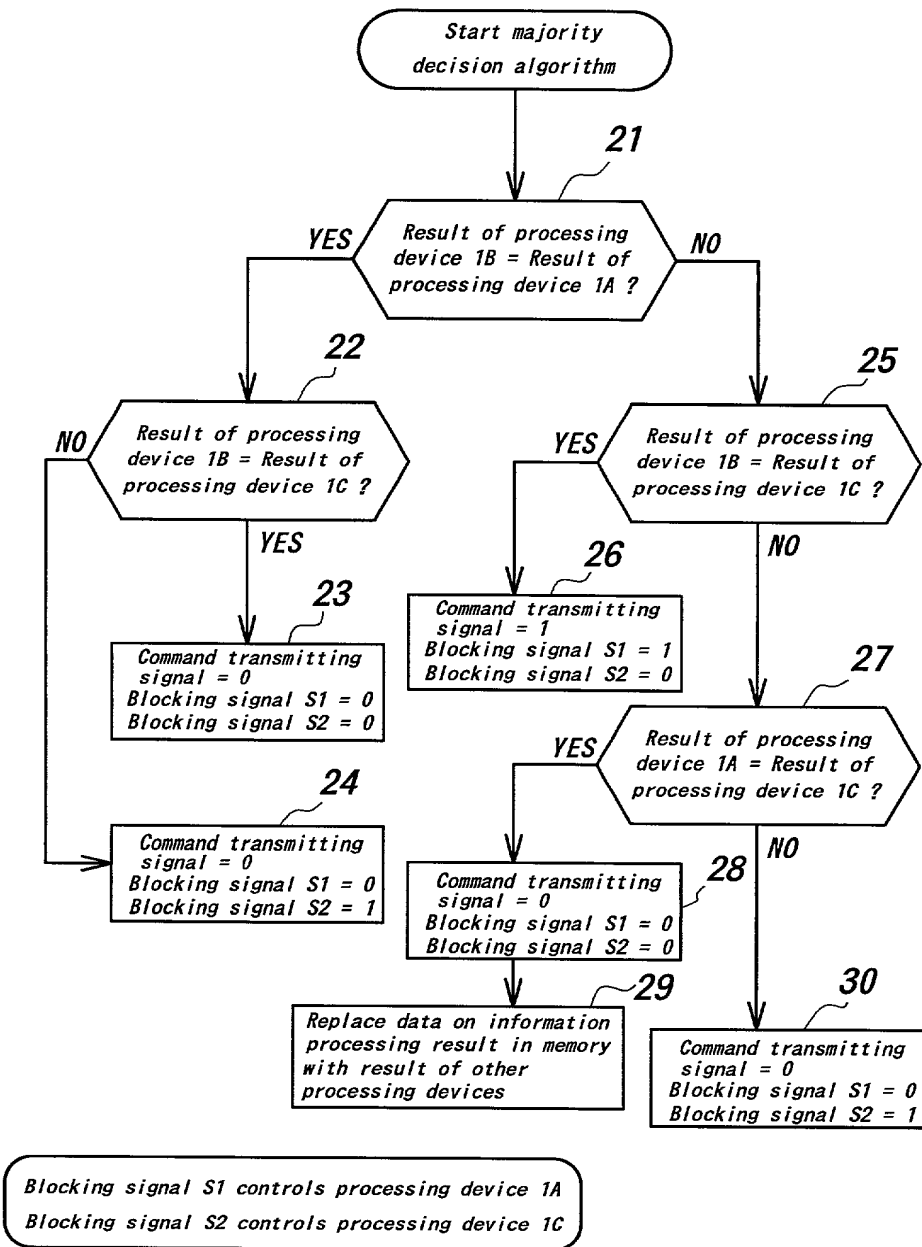


# FIG. 4

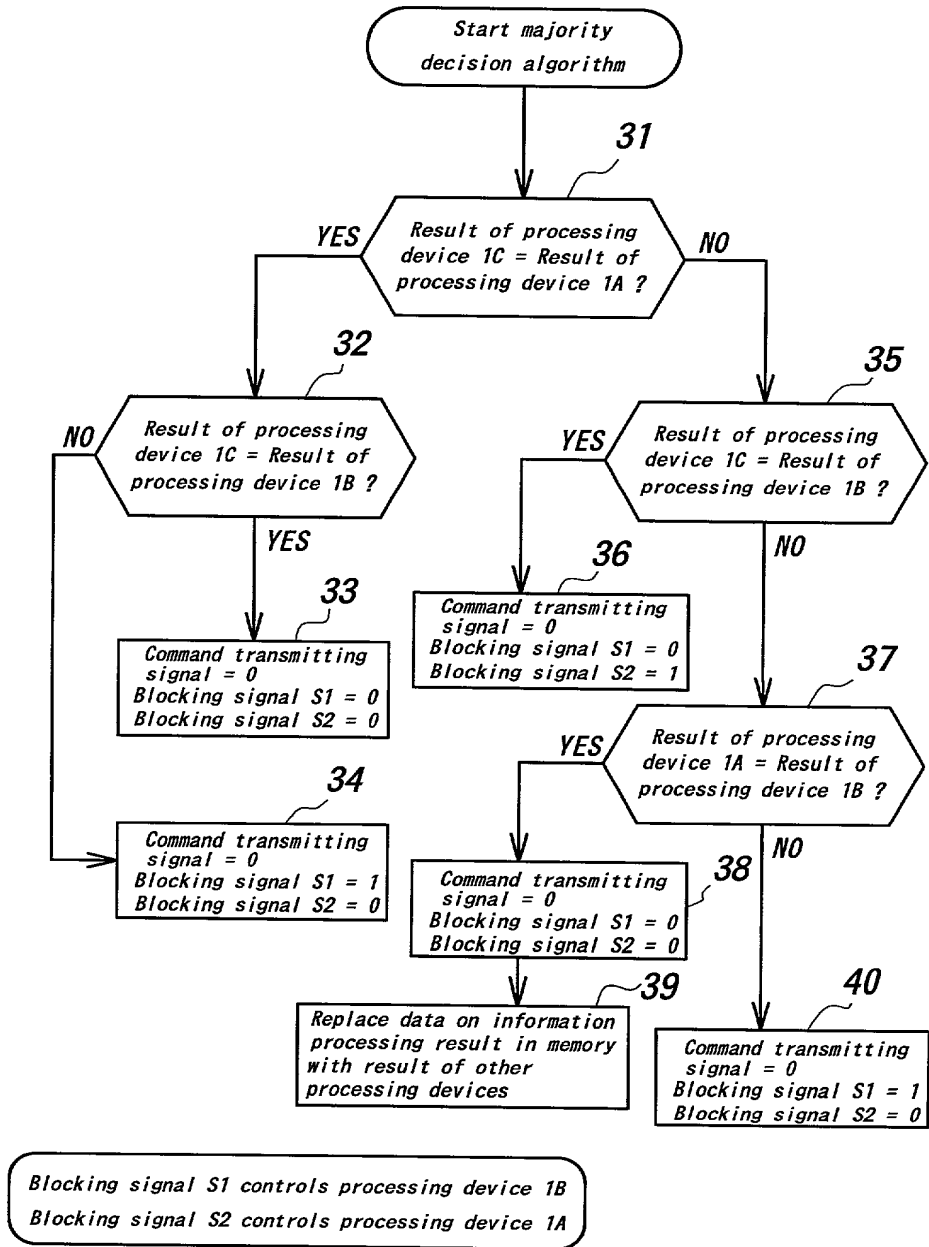


Blocking signal S1 controls processing device 1C  
Blocking signal S2 controls processing device 1B

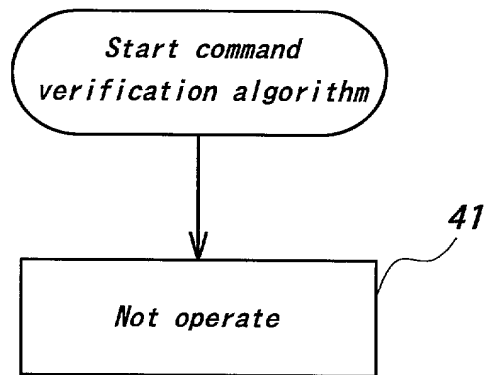
# FIG. 5



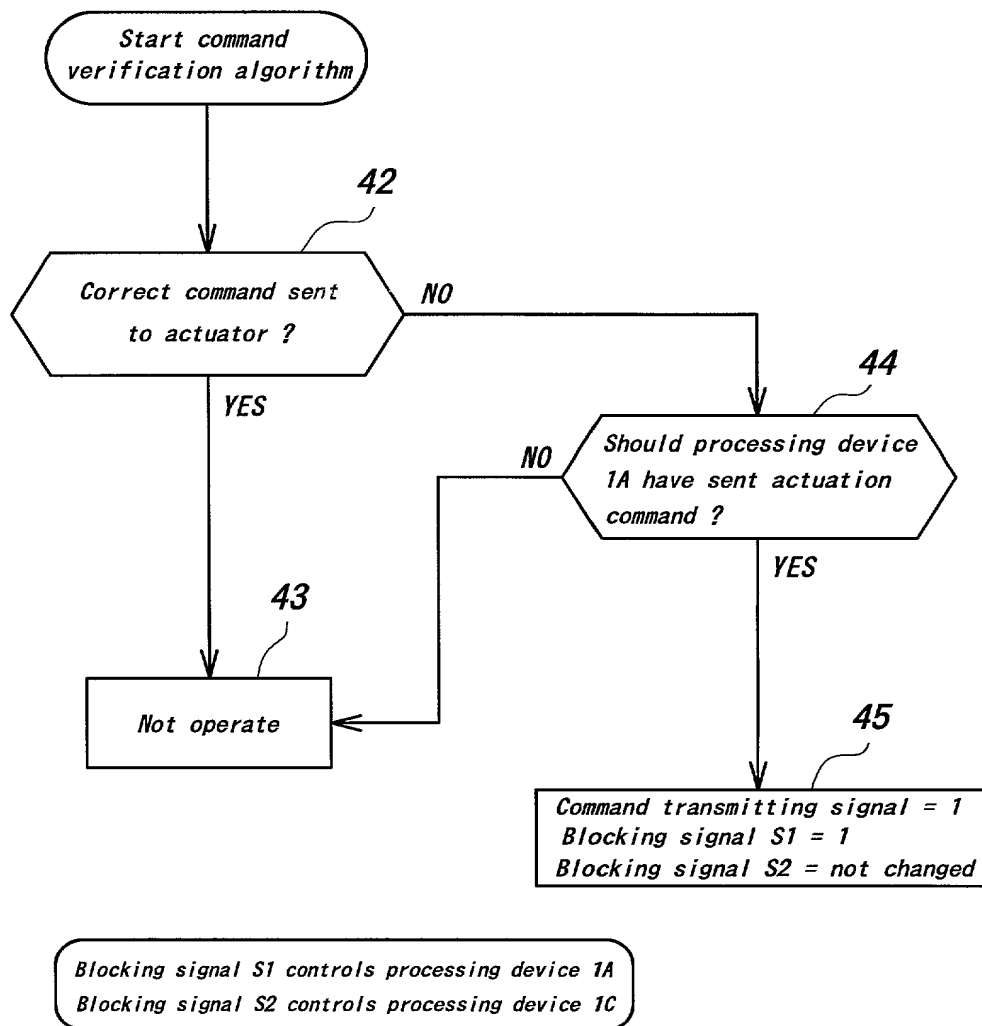
# FIG. 6



**FIG. 7**

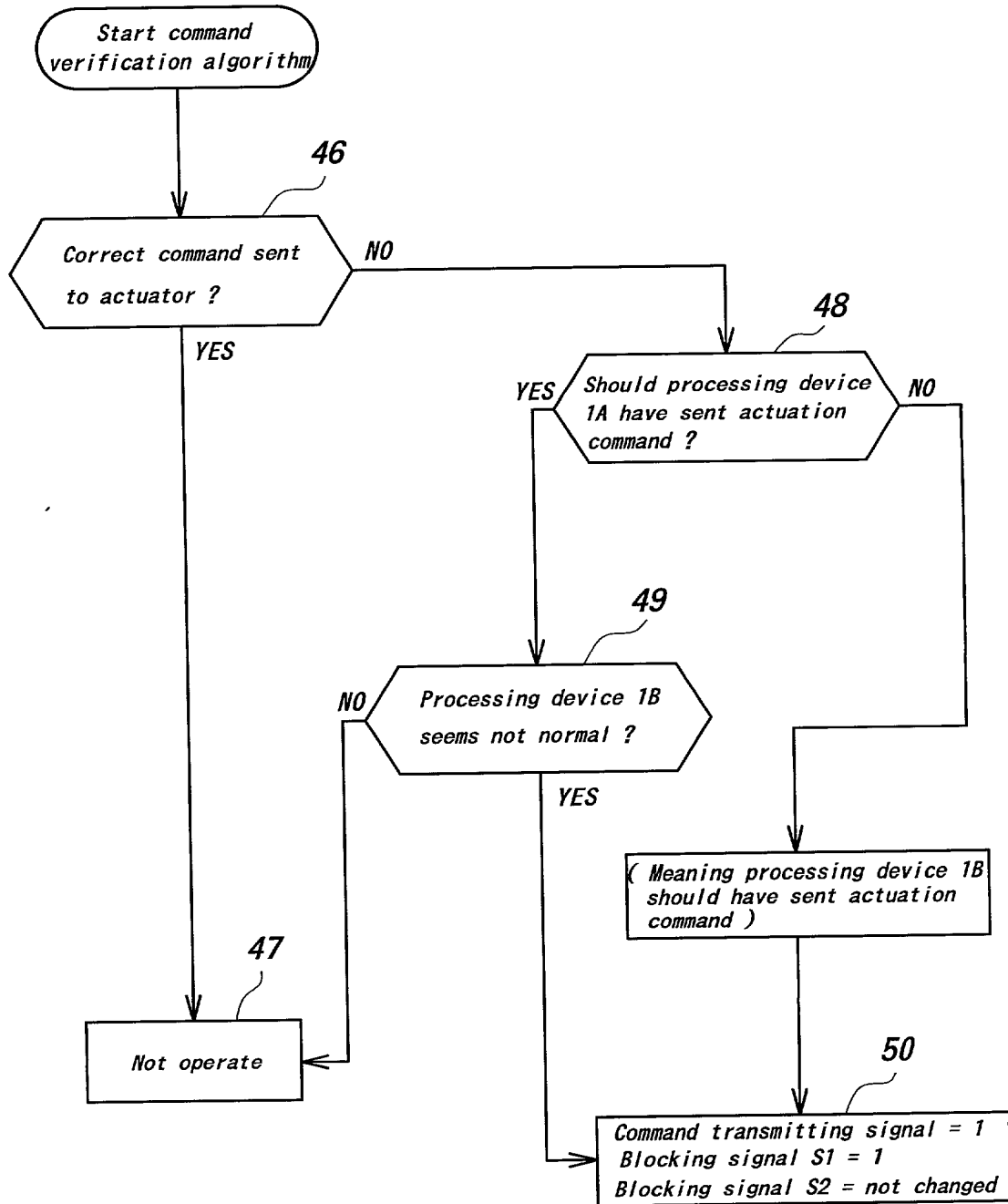


**FIG. 8**





# FIG. 9



Blocking signal S1 controls processing device 1B  
Blocking signal S2 controls processing device 1A